



# INFORMATION DISCLOSURE CITATION

PTO-1449

Complete if Known

Application No.	09/929,153
Filing Date	8/13/2001
First Named Inventor	Jyh-Ming Jong
Group Art Unit	2633
Atty. Docket No.	SUN-P5892-RJL

## U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
DS	5,652,767	Jul. 29, 1997	Kiyonaga et al.	375	317	Mar. 17, 1995
DS	5,898,741	Apr. 27, 1999	Nagashima	375	347	Jun. 19, 1997

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MAR 23 2004

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## FOREIGN PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						Yes	No
DS	2,246,677A	Feb. 5, 1992	GB	H04J	14/02	<input type="checkbox"/>	<input checked="" type="checkbox"/>
DS	0,996,262A	Apr. 26, 2000	EP	H04L	25/14	<input type="checkbox"/>	<input checked="" type="checkbox"/>
						<input type="checkbox"/>	<input type="checkbox"/>

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

DS	A Monolithic 480 MB/s Parallel AGC/Decision/Clock-Recovery Circuit in 1.2µm CMOS, Timothy H. Hu and Paul R. Gray, December 1993, IEEE Journal of Solid-State Circuits, Vol 28, No. 12

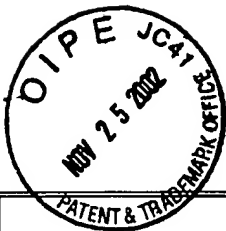
EXAMINER

Dalziel Singh

DATE CONSIDERED

10/26/04

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.



# INFORMATION DISCLOSURE CITATION

PTO-1449

ATTY. DOCKET NO.  
SUN-P5892-RJL

APPLICATION NO.  
09/929,153

APPLICANTS  
Jyh-Ming Jong, Drew G. Doblar and Daniel R. Cassiday

FILING DATE  
8/13/2001

GROUP ART UNIT  
2633

## U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
DS	5,982,210	Nov. 9, 1999	Rogers	327	156	Sep. 2, 1994
DS	US2001/0021051A1	Sep. 13, 2001	Kim	359	161	Dec. 29, 2000

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						Yes	No

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PTO-1449

ATTY. DOCKET NO.  
SUN-P5892-RJL

APPLICATION NO.  
Unknown

APPLICANTS  
Jyh-Ming Jong, Drew G. Doblar, and Daniel R. Cassiday

FILING DATE  
August 13, 2001

GROUP ART UNIT  
~~Unknown~~ 2633

## U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
DS	6,181,757	Jan. 30, 2001	Neumann	375	375	Apr. 27, 1998
DS	6,178,212	Jan. 23, 2001	Akashi	375	355	Mar. 16, 1998
DS	5,991,339	Nov. 23, 1999	Bazes	375	232	Jan. 16, 1998

## FOREIGN PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						Yes	No

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

DS	InfiniBand Architecture Release 1.0a, Jun. 19, 2001, Chapter 6: High Speed Electrical Signaling – 2.5 Gb/s
DS	InfiniBand Architecture Release 1.0a, Jun. 19, 2001, Chapter 8: Fiber Attachment – 2.5 Gb/s

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